

# NI Summer School 10.09. - 14.09.2012

## FPGA and High Performance Computing Technologies

	Mon., September 10th	Tues., September 11th	Weds., September 12th	Thurs., September 13th	Fri., September 14th		
09:00 - 09:30	Welcome and Keynote	Real-Time Math Algorithm Development	Selecting the right software architecture	GPU Computing with LV	FPGA-based Feedback Control of a Single Atom Trajectory		
09:30 - 10:00	Coffee Break						
10:00 - 10:30	Coffee Break	Coffee Break	Coffee Break	Coffee Break	Coffee Break		
10:30 - 11:00	LV RT LV Core 1	LV RT ctd. LV Core 1 ctd.	LV FPGA LV Core 2	LV FPGA ctd. LV Core 2 ctd.	Soft Skill Training for CCQED fellows		
11:00 - 11:30						LV RT/FPGA ctd.	Meet the experts
11:30 - 12:00							
12:00 - 12:30	Lunch Break						
12:30 - 13:00	Lunch Break						
13:00 - 13:30	Lunch Break						
13:30 - 14:00	LV RT ctd. LV Core 1 ctd.	LV RT ctd. LV Core 1 ctd.	LV FPGA ctd. LV Core 2 ctd.	LV FPGA ctd. LV RT/FPGA	Soft Skill Training ctd.		
14:30 - 15:00						LV RT/FPGA ctd.	Meet the experts ctd.
15:00 - 15:30							
15:30 - 16:00	Coffee Break	Coffee Break	Trip to Gräfelfing	Coffee Break	Coffee Break		
16:00 - 16:30	LV RT ctd. LV Core 1 ctd.	LV RT ctd. LV Core 1 ctd.	Excursion Toptica	LV FPGA ctd. LV RT/FPGA ctd.	Soft Skill Training ctd.		
16:30 - 17:00							
17:00 - 17:30							
17:30 - 18:00	Come Together	Poster Session	Trip to Garching	CLAD Certification			
18:00 - 18:30			Excursion MPQ			Social Event	
18:30 - 19:00							
19:00 - 19:30							
19:30 - 20:00							
20:00 - 20:30							

experienced track
beginner track